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Docket No.: 57454-311

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of

Shunsuke ENDOU

Serial No.:

Group Art Unit:

Filed: December 03, 2001

Examiner:

For: SEMICONDUCTOR DEVICE CAPABLE OF EASILY SETTING TEST MODE
DURING TEST CONDUCTED BY APPLYING HIGH VOLTAGE



INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents
Washington, DC 20231

Dear Sir:

In accordance with the provisions of 37 C.F.R. 1.56, 1.97 and 1.98, the attention of the Patent and Trademark Office is hereby directed to the documents listed on the attached form PTO-1449. It is respectfully requested that the documents be expressly considered during the prosecution of this application, and that the documents be made of record therein and appear among the "References Cited" on any patent to issue therefrom.

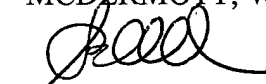
This Information Disclosure Statement is being filed within three months of the U.S. filing date OR before the mailing date of a first Office Action on the merits. No certification or fee is required.

Serial No.:

Each non-English reference is accompanied by an English Abstract.

Respectfully submitted,

MCDERMOTT, WILL & EMERY



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